EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((priority or load) with (power or sleep or turn off))	US-PGPUB; USPAT	ADJ	OFF	2007/12/04 15:20
S2	258	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((power or sleep or turn off))	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 10:58
S3	32	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off))	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:03
S4	486	710/58.ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/12/04 15:25
S5	3190	713/300.ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/12/05 09:17
S6	3133	713/300.ccls. and power	US-PGPUB; USPAT	ADJ	OFF	2007/12/05 09:17
S7	708	713/300.ccls. and ((control or register) with (power off or power down or shut down or sleep or turn off))	US-PGPUB; USPAT	ADJ	OFF	2007/12/05 09:19
S9	1746	713/320.ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/12/05 09:19
S10	951	713/324.ccls.	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 12:59
S11	8095	processor with (queue or FIFO or buffer) with (controller or monitor or workload)\	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:00
S12	8095	processor with (queue or FIFO or buffer) with (controller or monitor or workload)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:00
S13	142	processor with (queue or FIFO or buffer) with (controller or monitor) with (power or consumption)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:16
S14	0	(multiple processors or co-processor or one of many processor) with (queue or FIFO or buffer) with (controller or monitor) with (power or consumption)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:44

EAST Search History

S15	0	(multiple processors or co-processor or one of many processor) with (queue or FIFO or buffer) with (controller or monitor) same (power or consumption)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:45
S16	59	(multiple processors or co-processor or one of many processor) with (queue or FIFO or buffer) with (controller or monitor)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:45
S18	0	710/324.ccls, and (((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same (control or register))	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:47
S19	0	710/324.ccls. and (((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same (control or register))	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:47
S20	0	710/324.ccls. and (((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)))	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:48
S21	0	710/324.ccls. and ((co processor or slave processor or sub processor) with (processor or main processor or CPU or central processing unit))	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:48
S22	101	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((controller or register or buffer) and (power or sleep or turn off))	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 13:59
S23	750	(processor or co processor or muliple processor) same (controller or monitor or logic circuit) same (queue or FIFO or register or buffer) same (power down or sleep or idle)	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 14:01
S24	0	((processor or co processor or muliple processor) same (controller or monitor or logic circuit) same (queue or FIFO or register or buffer) same (power down or sleep or idle)).ti.	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 14:01

EAST Search History

S25	37	((processor or co processor or muliple processor) same (controller or monitor or logic circuit) same (queue or FIFO or register or buffer) same (power down or sleep or idle)).clm.	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 14:01
S26	15	((processor or co processor or muliple processor) same (controller or monitor or logic circuit) same (queue or FIFO or register or buffer) same (power down or sleep or idle)).ab.	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 14:02
S27	3	S25 and S26	US-PGPUB; USPAT	ADJ	OFF	2007/12/06 14:02
S28	16	"5,737,615"	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 10:58
S29	13	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and buffer	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:07
S30	1	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and buffer and bandwidth	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:07
S31	0	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and buffer and (maximum with rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:08
S32	0	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and buffer and (data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:08
S33	0	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and (data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:08

EAST Search History

S34	10	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and (rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:10
S35	1	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) same (buffer or work queue or workload or work load or queue) and rate	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:12
S36	0	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and (buffer or work queue or workload or work load or queue) and (maximum with data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:13
S37	0	((co processor or slave processor or sub processor) with (main processor or CPU or central processing unit)) same ((control or register) with (power or sleep or turn off)) and (buffer or work queue or workload or work load or queue) and ((maximumor threshold) with data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:13
S38	94	(main processor or CPU or central processing unit)) same ((control or register) and (buffer or work queue or workload or work load or queue) and ((maximum or threshold) with data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:51
S39	234	(processor or CPU or central processing unit)) same ((control$3 or register) and (buffer or work queue or workload or work load or queue) and ((maximum or threshold) with data rate)	US-PGPUB; USPAT	ADJ	OFF	2007/12/07 11:58
S40	350	processor with (buffer or memory) with (work queue or workload or work load)	US-PGPUB; USPAT	ADJ	OFF	2007/12/14 11:06
S41	2	(multi or multiple or co) processor with (buffer or memory) with (work queue or workload or work load)	US-PGPUB; USPAT	ADJ	OFF	2007/12/14 13:34

S42	350	processor with (buffer or memory) with (work queue or workload or work load)	US-PGPUB; USPAT	ADJ	OFF	2007/12/14 15:24